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Serial No	o. by DEC 0 1		g Date		mine	7			o. 10319-112 Art Unit	
10/676,862 <b>(5) (5)</b> Eptember 30, 2003										
Inventor( So et al.	· ' <b>V</b> P.	0 0 Car 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6								
Title of Ir	Title of Invention  Method and System for Temperature Compensation for Memory Cells with Temperature-Dependent Behavior									
	TO THE COMMISSIONER FOR PATENTS									
Reference	Transmitted herewith is <u>Transmittal Letter (in duplicate)</u> ; <u>Information Disclosure Statement</u> ; <u>Form PTO 1449</u> ; <u>10</u>									
TCICICIN	erences; and Postcard Receipt.									
	Small entity status of this application under 37 CFR § 1.27 has been established by verified statement previously submitted.									
	A verified staten	nent to estat	olish small entity sta	tus under 37 (	CFR (	§§ 1.9 and 1.27	is enclosed	l.		
	Petition for a _m	onth extensi	on of time.							
$\boxtimes$	No additional fee	e is required	l ·							
	The fee has bee	n calculated	as shown below:							
						Small	Entity	_	Other Small	
	Claims		Highest No.					or		
	Remaining		Previously	Present		Data	Add'l		Data	Add'I
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	Please charge E	•	ount No. 23-1925 (E	RINKS HOFE	R GI	LSON & LION	E) in the am	ount (	of \$ A	duplicate
			o cover the filing fee	e is enclosed.						
$\square$			y authorized to char		fany	additional filing	r food roquir	od un	dor 27 CED 8	1 16 and
	any patent app	lication pro	cessing fees undecount No. 23-1925.	r 37 CFR §	1.17	7 associated	with this co			
$\boxtimes$										
	Respectfully submitted,									
	Joseph F. Heb Registration No. 41,070									
BRINKS	HOFER GII SON	& LIONE		Attorney fo	r App	olicant				
P.O. BO	BRINKS HOFER GILSON & LIONE P.O. BOX 10395									
	CHICAGO, ILLINOIS 60610 (312) 321-4200									
	I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents									
P.O. Box 1450, Alexandria, VA 22313-1450, on November 25, 2003.										

Date: November 25, 2003

rev. Dec.-00 Document2 Signature:

DEC 0 1 2003

I hereby sertify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on November 25, 2003

Date of Deposit

Joseph F. Hetz - Reg. No. 41,070

Name of Applicant, Assignee or

Registered Representative

Signature

Patent Our Case No. 10519-112

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicat	ion of: So et al	)
		)
Serial No.:	10/676,862	)
		)
Filed:	September 30, 2003	)
		)
For:	Method and System for	)
	Temperature Compensation for	)
	Memory Cells with Temperature-	)
	Dependent Behavior	)

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97-1.99, Applicants hereby submit documents A1-A58 listed on the attached form PTO-1449 for consideration by the Examiner. In accordance with the August 5, 2003 OG Notice, copies of the U.S. patents and patent application publications are not provided. Copies of the ten other art

documents are enclosed herewith. Applicants request that the Examiner review the entire disclosure of these documents and make them of record.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

Dated: November 25, 2003

Respectfully submitted,

Joseph F. Hetz Reg. No. 41,070

Attorney for Applicants

BRINKS HOFER GILSON & LIONE P.O. Box 10395 Chicago, Illinois 60610 (312) 321-4719

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FORM PTO-1449	SERIAL NO.	CASE NO.
Man a significant and the	10/676,862	10519-112
LIST OF PATENTS AND PUBLICATIONS FOR	FILING DATE	GROUP ART UNIT
APPLICANT'S INFORMATION DISCLOSURE	September 30, 2003	
STATEMENT		
(use several sheets if necessary)	APPLICANT(S): So et al.	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	5,784,328	07/1998	Irrinki et al.		
	A2	5,149,199	09/1992	Kinoshita et al.		
	A3	5,977,746	11/1999	Hershberger et al.		
	A4	5,890,100	03/1999	Crayford		
	A5	US 2003/0046020 A1	03/2003	Scheuerlein		
	A6	6,525,953 B1	02/2003	Johnson		
	A7	6,185,121 B1	02/2001	O'Neill		
	A8	5,652,722	07/1997	Whitefield		
	A9	6,246,610 B1	06/2001	Han et al.		
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	A11	US/2002/0136047 A1	09/2002	Scheuerlein		
	A12	5,835,396	11/1998	Zhang		
	A13	6,034,882	03/2000	Johnson et al.		
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	A19	4,646,266	02/1987	Ovshinsky et al.		
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	A24	5,925,996	07/20/99	Murray		,
	A25	6,055,180	04/25/00	Gudesen et al.		
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	A31	5,383,157	01/17/95	Phelan		
	A32	4,744,061	05/10/88	Takemae et al.		
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	A35	6,070,222	05/30/00	Farmwald et al.		
	A36	6,212,121	04/03/01	Ryu et al.		

EXAMINER	DATE CONSIDERED	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

/ 5		Page 2 of 2
FORM PTO-1449g DEC 0 1 2003 1	SERIAL NO.	CASE NO.
	10/676,862	10519-112
LIST OF PAKENTS AND PUBLICATIONS FOR	FILING DATE	GROUP ART UNIT
APPLICANT'S INFORMATION DISCLOSURE	September 30, 2003	
STATEMENT		
(use several sheets if necessary)	APPLICANT(S): So et al.	

## REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A37	4,698,788	10/06/87	Flannagan et al.		
	A38	5,276,649	01/04/94	Hoshita et al.	·	
	A39	6,385,074 B1	05/2002	Johnson et al.		
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	A43	5,410,512	04/1995	Takase et al.		
	A44	5,940,340	08/1999	Ware et al.		
	A45	4,592,027	05/1986	Masaki		
	A46	US 2003/0043643 A1	03/2003	Scheuerlein et al.		
	A47	6,507,238	01/2003	Yang		
	A48	6,577,549	06/2003	Tran et al.		

EXAMINER						
INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)					
	A49	"A 14ns 1Mb CMOS SRAM with Variable Bit-Organization," Wada et al., 1988 IEEE				
		International Solid-State Circuits Conference, pages 252-253 (February 19, 1988).				
	A50	"Partial Selection of Passive Element Memory Cell Sub-Arrays for Write Operation," U.S.				
		Patent Application Serial No. 09/748,649, filed December 22, 2000; inventors: Roy E.				
		Scheuerlein and Matthew P. Crowley.				
	A51	"64M x 8 Bit NAND Flash Memory," Samsung Electronics (October 27, 2000).				
	A52	"How Flash Memory Works," wysiwyg://8/http://www.howstuffworks.com/flash-				
		memory.htm?printable=1, 5 pages (1998).				
	A53	"Datalight FlashFX™ 4.06 User's Guide," page 11 (August 2000).				
	A54	"How Does TrueFFS® manage Wear Leveling?," http://www.m-				
		sys.com/content/information/calcInfo.asp, 2 pages (printed October 5, 2001)				
	A55	"A CMOS Bandgap Reference Circuit with Sub-1V Operation," IEEE Journal of Solid-State				
		Circuits, Vol. 34, No. 5, May 1999, pages 670-674.				
	A56	"Sub-1V CMOS Proportional to Absolute Temperature References," IEEE Journal of Solid-				
		State Circuits, Vol. 38, No.1, January 2003, pages 85-89.				
	A57	"The flash memory read path: building blocks and critical aspects," Micheloni et al.,				
	L	Proceedings of the IEEE, Vol. 91, No. 4, April 2003, pages 537-553.				
	A58	"A current-based reference-generations scheme for 1T-1C ferroelectric random-access				
		memories," Siu et al., IEEE Journal of Solid-State Circuits, Volume 38 Issue 3, March 2003,				
		pages 541-549.				

EXAMINER	DATE CONSIDERED	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.